Docket No. 244612US2CONT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Takashi MIYAMORI

GAU:

SERIAL NO: NEW CONTINUATION APPLICATION

EXAMINER:

FILED: HEREWITH

FOR: PROCESSOR FOR IMPROVING INSTRUCTION UTILIZATION USING MULTIPLE PARALLEL PROCESSORS AND COMPUTER SYSTEM EQUIPPED WITH THE PROCESSOR

REQUEST FOR PRIORITY

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

■ Full benefit of the filing date of U.S. Application Serial Number 09/604,907, filed June 28, 2000, is claimed pursuant to the provisions of **35 U.S.C. §120.**

☐ Full benefit of the filing date(s) of U.S. Provisional Application(s) is claimed pursuant to the provisions of **35 U.S.C. §119(e):**   **Application No.**   **Date Filed**

■ Applicants claim any right to priority from any earlier filed applications to which they may be entitled pursuant to the provisions of **35 U.S.C. §119**, as noted below.

In the matter of the above-identified application for patent, notice is hereby given that the applicants claim as priority:

COUNTRY	APPLICATION NUMBER	MONTH/DAY/YEAR
Japan	11-184247	June 29, 1999
Japan	2000-191604	June 26, 2000

Certified copies of the corresponding Convention Application(s)

☐ are submitted herewith

☐ will be submitted prior to payment of the Final Fee

■ were filed in prior application Serial No. 09/604,907 filed June 28, 2000.

☐ were submitted to the International Bureau in PCT Application Number
Receipt of the certified copies by the International Bureau in a timely manner under PCT Rule 17.1(a) has been acknowledged as evidenced by the attached PCT/IB/304.

☐ (A) Application Serial No.(s) were filed in prior application Serial No.   filed   ; and

☐ (B) Application Serial No.(s)

    ☐ are submitted herewith

    ☐ will be submitted prior to payment of the Final Fee

Respectfully Submitted,

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